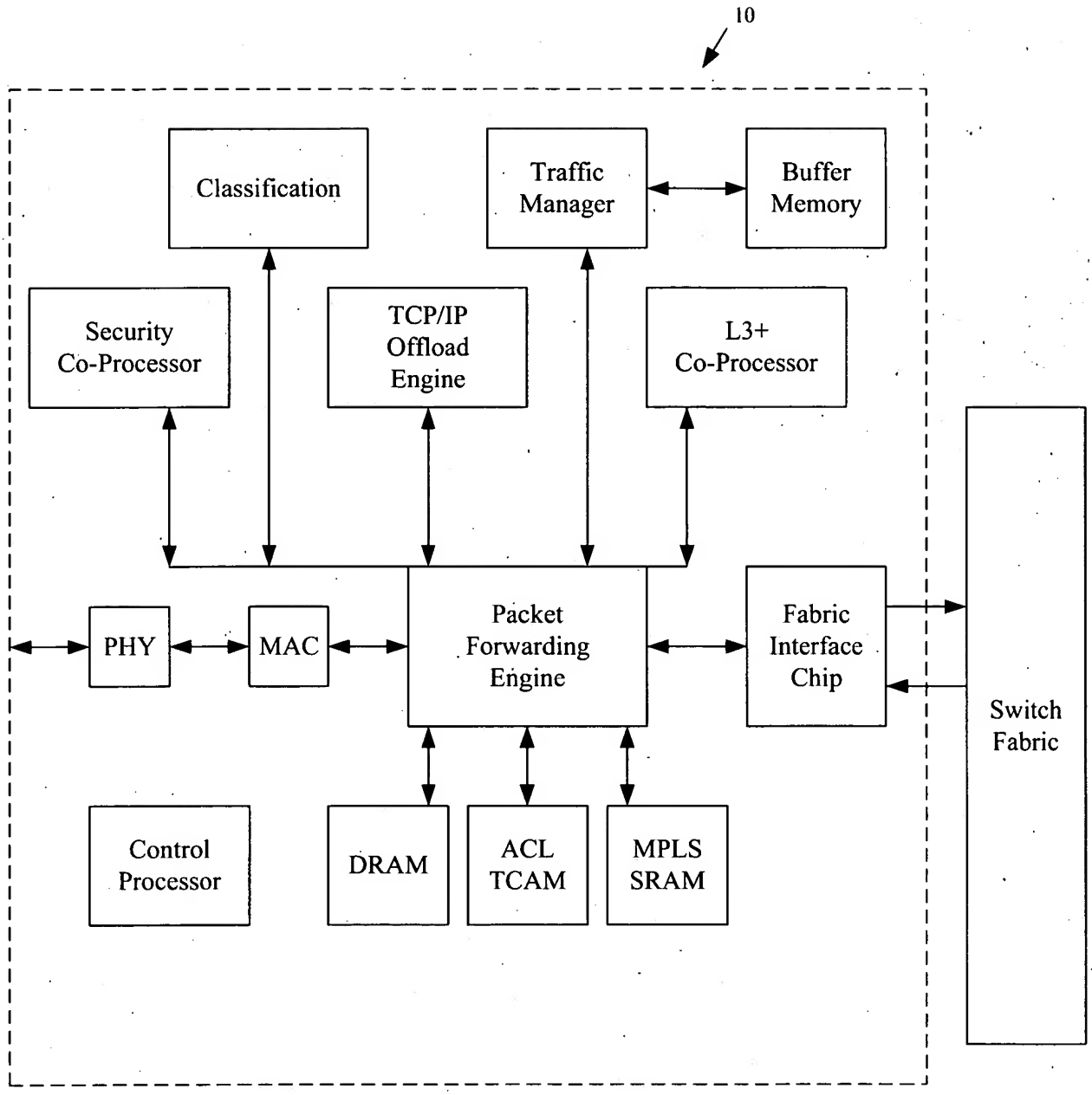

Figure 1
(Conventional)

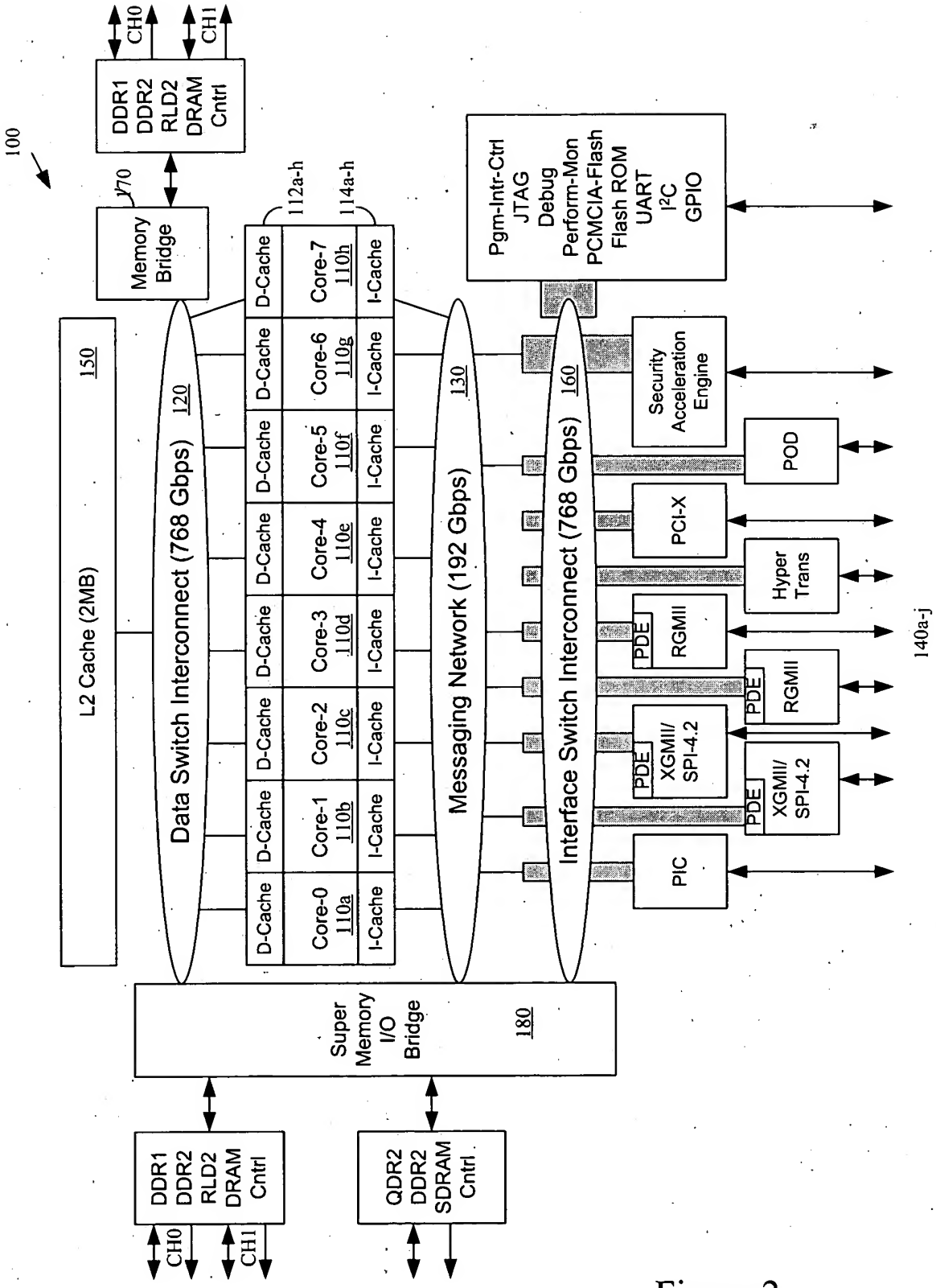
100
DDR1 DDR2 RLD2 DRAM Cntrl
CH0
CH1
170
Memory Bridge
112a-h
114a-h
Core-0 110a
Core-1 110b
Core-2 110c
Core-3 110d
Core-4 110e
Core-5 110f
Core-6 110g
Core-7 110h
D-Cache
I-Cache
L2 Cache (2MB) 150
Data Switch Interconnect (768 Gbps) 120
Messaging Network (192 Gbps) 130
Interface Switch Interconnect (768 Gbps) 160
Pgm-Intr-Ctrl
JTAG
Debug
Perform-Mon
PCMCIA-Flash
Flash ROM
UART
I2C
GPIO
Security Acceleration Engine
POD
PCI-X
Hyper Trans
PDE
RGMII
XGMII/ SPI-4.2
PIC
140a-j
Super Memory I/O Bridge 180
DDR1 DDR2 RLD2 DRAM Cntrl
QDR2 DDR2 SDRAM Cntrl

Figure 2